

**RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2100**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor :	Khosro Khakzadi	Conf. No. 1314
Appln. No.:	10/719,673	
Filed :	November 21, 2003	Group Art Unit: 2179
For :	CHIP DESIGN COMMAND PROCESSOR	Examiner: WIENER, Eric A.
Docket No.:	03-1862 / L13.12-0251	

---

**AMENDMENT AFTER FINAL**

**Mail Stop AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

**FILED ELECTRONICALLY  
AUGUST 12, 2008**

This is in response to the final Office Action mailed on July 9, 2008. Please amend the above-identified application as follows.